

The Planar Magneto-Transistor: Topology & Offset

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Received: January 27, 2014

Accepted: February 12, 2014

Online Published: February 21, 2014

doi:10.5539/mas.v8n2p56

URL: <http://dx.doi.org/10.5539/mas.v8n2p56>

Abstract

We have studied the planar magneto-transistor and experimentally measured the offset to better understand the device. The geometry of the electrodes of a planar magneto-transistor is an essential factor for creating high-performance magneto-transistors. Acquiring an adequate understanding of the device's underlying mechanisms should help to improve its offset.

Keywords: planar magneto-transistor, sensitivity, offset

1. Introduction

Dual collector magneto-transistors have been studied in great detail (Hudson, 1968; Davies & Wells, 1971; Vikulin, Glauberman, Vikulina, & Zaporozhchenko, 1974; Vinal & Masnari, 1982; Kordic, Zieren, & Middlehoek, 1983; Baltés & Popovic, 1986; Cristoloveanu, 1981; Mitnikova, Persiyarov, Rekalova, & Shtyubner, 1978; Andreou & Westgate, 1984; Ristic, Smy, & Baltés, 1988; Nathan, Maenaka, Allegretto, Baltés, & T Nakamura, 1989; Roumenin, 1994; Riccobene, Gartner, Wachutka, Baltés, & Fichtner, 1995; Chaplygin, Galushkov, Romanov, & Volkov, 1995; Kang, Lee, & Han, 1996; Metz, 1999; Nagy & Trujillo, 1998; Amelichev, Galushkov, Mirgorodskii, Tikhomirov, Chaplygin, Shorin, & Shubin, 1999; Vikulin, I., Vikulina, L., & Stafeev, 2001; Tikhonov, 2005; Oxland, Long, & Rahman, 2009; Hnatiuc & Căruntu, 2009; Leepattarapongpan, Phetchakul, Penpondee, Pengpad, Chaowicharat, Hruanun, & Poyai, 2010; Amelichev, Tikhonov, & Cheremisinov, 2013; Tikhonov, 2013); however, they are not in use because of a large initial potential imbalance. The analysis (Metz, 1999) of this imbalance was based on the assumption of asymmetric structure. The following was written in conclusion.

“Typical offsets in the MTs chosen as reference are up to a few percent. The offsets vary between individual devices. This variation usually dominates the statistics. A strong local mismatch of the offset between neighboring devices is observed. In addition to this scale variation, variations on a larger scale also contribute to offset. Suppressed sidewall injection magneto-transistors (SSIMT) exhibit a quadrupling of the offsets compared to standard MTs. This comes from misalignment of the emitter guard ring. By applying offset reduction approaches, average absolute values as low as 0.11 % are achieved, corresponding to magnetic field equivalent offset of 3.2 mT and 3.9 mT depending on the device. This is lower by a factor of approximately 100 than the worst case equivalent offsets observed in the SSIMTs integrated in the angle detection system”. An epigraph is a philosophical phrase H. Weyl “...we had to understand that general organization of nature possesses that [left-right] symmetry. But one will not expect that any special object of nature shows it to perfection.”

Tikhonov, Kozlov and Polomoshnov (2008), Tikhonov (2009, 2010a) have demonstrated that transistor current is dependent on the value and sign of the imbalance between collector potentials. This effect is not explained by initial asymmetries. The cause of this initial imbalance remains a mystery in the literature. Mathematical modelling makes it possible to calculate the origin for a given skewness, but this modelling does not predict the potential imbalance. We turn to experimental methods to help determine the origin of this imbalance.

There are no known studies that explore the potential imbalance of planar magneto-transistors. Lateral magneto-transistors are sensitive to magnetic fields directed parallel to the surface of a crystal, while planar magneto-transistors are sensitive to magnetic fields directed to the orthogonal axis of a crystal. These field direction sensitivities are responsible for the different characteristics present in lateral and planar magneto-transistors. This paper presents the results of an experimental study of planar magneto-transistors obtained in a single technological cycle and on the same plates with lateral magneto-transistors.

2. The Structure of Planar Magneto-Transistors

All of the transistors used the same body structure developed for the lateral dual-collector *n-p-n* bipolar magneto-transistor with a base in the well (Amelichev et al., 2013). The planar transistors used several different topological configurations of electrodes, emitters, collectors and contacts to the base (Figure 1). The designs took into account the current lines of injected carriers in the bipolar magneto-transistor under the action of the Lorentz force (Tikhonov, 2010b; Tikhonov, Kozlov, & Polomoshnov, 2010).

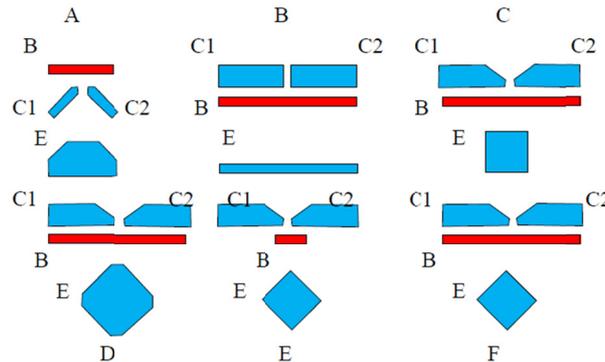


Figure 1. Topology of planar magneto-transistors

Magneto-transistor A had an octagonal emitter with an area of $1000 \mu^2$ and eight pairs of collectors. The collectors were arranged with opposite flat sides at an angle of 45° relative to the emitter. Similar collectors of each pair were joined via metallization. The distance between the emitter and the collectors, L_{EC} , was 27μ . A square frame next to the base functioned as a heavily doped contact area between the base and the collectors. From this point we will use the following abbreviations: C1-first collector; B-base; E-emitter; C2-second collector.

Magneto-transistor B is a full analogue of the lateral transistor with a 280μ length of strip electrodes. The distance between the emitter and the collectors was 50μ and the emitter had an area of $1000 \mu^2$. Contacts to the base were located between the emitter and the collectors. Each collector was divided into two parts that were cross-connected via metallization.

Magneto-transistor C had a square emitter with an area of $1000 \mu^2$. The side of the emitter was parallel to the blade edge of the collector and the L_{EC} was 27μ . A doped region base formed a single border around the emitter.

Magneto-transistor D had a rhomboid emitter (a square rotated 45°) with truncated corners, an area of $2000 \mu^2$ and an L_{EC} of 22μ .

Magneto-transistor E had a rhomboid emitter (a square rotated 45°) with an area of $1000 \mu^2$ and an L_{EC} of 27μ . A heavily doped contact area to the base was located opposite the gap between a pair of collectors at the corners of the rhomboid emitter, and this doped area did not form a continuous ring.

Magneto-transistor F had a rhomboid emitter (a square rotated 45°) with an area of $1000 \mu^2$. Four pairs of collectors were positioned at opposite angles of the emitter so that they formed an angle of 45° with the emitter. One collector of each pair was connected with the corresponding collectors from the other pairs. A square frame to the base functioned as a heavily doped contact area between the emitter and collectors.

3. Offset of Planar Magneto-Transistors

The planar magneto-transistors were measured using the circuit shown in Figure 2, where the resistance in the load collectors R_{C1} and R_{C2} was $5,465 \text{ k}\Omega$, and the supply voltage, V_{dd} , was 9 V . Contact B to the base was not connected with the contact to the well contact W.

The collectors' potential, $U_{C1, C2}(0)$, initial imbalance of the two collectors' potential, $U_{C1}(0) - U_{C2}(0)$, and the signal in the magnetic field, $U_{C1}(B) - U_{C1}(0) - U_{C2}(B) + U_{C2}(0)$, were measured at a variable base potential, U_{IN} . Offset of the magneto-transistor - equivalent magnetic field [31] - is calculated using a formula.

$$B_{\text{equiv}} = [U_{C1}(0) - U_{C2}(0)] \cdot B / [U_{C1}(B) - U_{C1}(0) - U_{C2}(B) + U_{C2}(0)]$$

The measurement of the planar transistors was made in a permanent magnetic field, which measured 120 mT .

When $U_{C1, C2} = 1.5 \text{ V}$ data are given in Table 1.

Table 1. Offset of planar magneto-transistors at $U_{C1,C2}=1.5$ V

| Magneto-transistor | $U_{C1(0)}-U_{C2(0)}$, mV | $U_{C1(B)} - U_{C1(0)}-U_{C2(B)}+U_{C2(0)}$, mV | B_{equiv} , T |
|--------------------|----------------------------|--|-----------------|
| A | 695 | 2 | 42 |
| B | 133 | 4 | 4 |
| C | 18,4 | 1,52 | 1,45 |
| D | 17,8 | 4 | 0,55 |
| E | 3,8 | 1,3 | 0,35 |
| F | 3,7 | 1,64 | 0,27 |

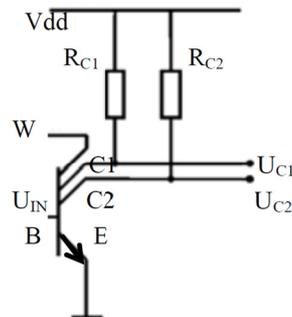


Figure 2. Biasing circuit for the planar magneto-transistor

The offsets of magneto-transistors A - F shown, as a function of the collector potential in Figure 3.

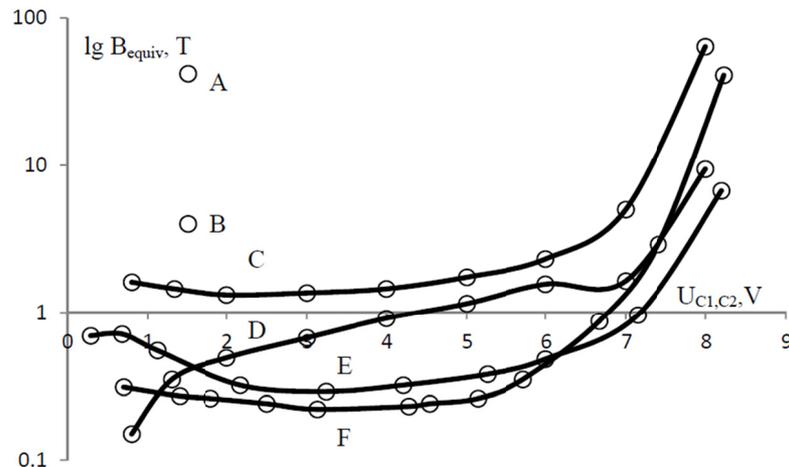


Figure 3. Offset of magneto-transistors, depending on the collector potential

Magneto-transistor A had a high initial imbalance between the two collectors' potential and a small signal in the magnetic field. The electric field was concentrated at the corners of the collectors and the injector. The offset was maximized by the heavily doped square-frame contact area next to the collectors.

Magneto-transistor B had five-times less initial imbalance than the two collector's potential and half more the useful signal in the magnetic field. The equivalent lateral magneto-transistor had a large offset.

Magneto-transistor C had an initial imbalance that was reduced twenty-five-fold and had half the magnetic field signal. The offset decreased when the emitter was in a square configuration.

Magneto-transistor D had the same offset observed in C and also had a reduced initial imbalance. The rhomboid emitter with truncated corners produced very interesting results. At low collector's voltage has a small potential imbalance.

Magneto-transistor E also had a small offset and the signal in magnetic field was more than twice the signal observed in D. However, this device configuration had more than three-times the initial imbalance of the two collectors' potential. The heavily doped contact area located between the emitter and the collectors did not form a continuous ring and, as a result, there was an increased offset.

Magneto-transistor F had the smallest offset. The combination of the rhomboid emitter and a heavily doped contact proved to be optimal.

In planar magneto-transistors, the magnetic field is perpendicular to the surface of the crystal. Sensitivity is determined by the variation of the current collector in a plane parallel to the surface of the crystal and by changing the effective length of the base.

4. Conclusion

The increase of the initial imbalance of the two collectors' potential is apparently the result of the increased electric field at the corners of the collectors, as spontaneous discharge lightning rod.

The rhombic emitter eliminates competition between the injectors from the angles of the square emitter.

The heavily doped contact area to the base, located between the emitter and the collectors, does not provide leak currents across the surface of the injection. This absence of leak currents is reflected in the reduced imbalance.

The geometry of the magneto-transistors played a vital role; the rhomboidal emitter electrodes, the rectangular collectors and the solid frame of the heavily doped contact contributed to reduce the offset.

It can be concluded that the geometry of the electrodes of a planar magneto-transistor is an essential factor for creating high-performance magneto-transistors. Optimization of the topology will allow magneto-transistors to be used by integrated magnetic sensors.

A small Discovery- figuring the influence of electrode shape on initial voltage imbalance potential collectors of magneto-transistor- can give a broader way to use magneto-transistors.

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